

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/701,301
Filing Date November 3, 2003
Inventor John T. Moore
Assignee Micron Technology, Inc.
Group Art Unit 2815
Examiner Unknown
Attorney Docket No. MI22-2438
Title: Methods of Forming Dielectric Materials and Methods of Processing
Semiconductor Substrates

Commissioner for Patents
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2. Supplemental Information Disclosure Statement w/PTO-1449

Dated: January 9, 2004

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated:

January 9, 2003

By:

Jennifer J. Taylor
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-2438		SERIAL NO. 10701301	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT John T. Moore			
				FILING DATE November 3, 2003		GROUP 2815	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,171,900 B1	01-2001	Sun			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
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OTHER REFERENCES (including Author, Title, Date, Permanent Pages, Etc.)							
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